

High-Performance IO-Link Master

FASTUS UR Series

16 Multifunctional IO-Link Master ports
Compact DIN-rail Mountable Unit



IO-Link

CC-Link **IE TSN**

CC-Link **IE Field Basic**

CC-Link **IE Field**

EtherNet/IP

Modbus TCP

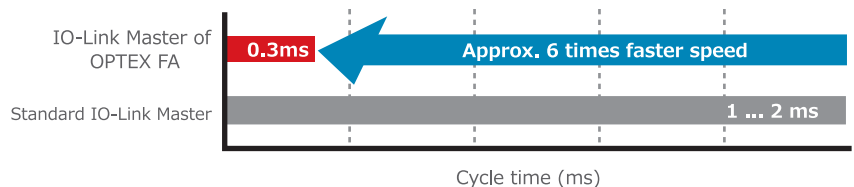
EtherCAT

PROFI® *
NET

{JSON}

* Coming soon

■ Ultra-fast 0.3 ms cycle
Between IO-Link Master
and IO-Link device*



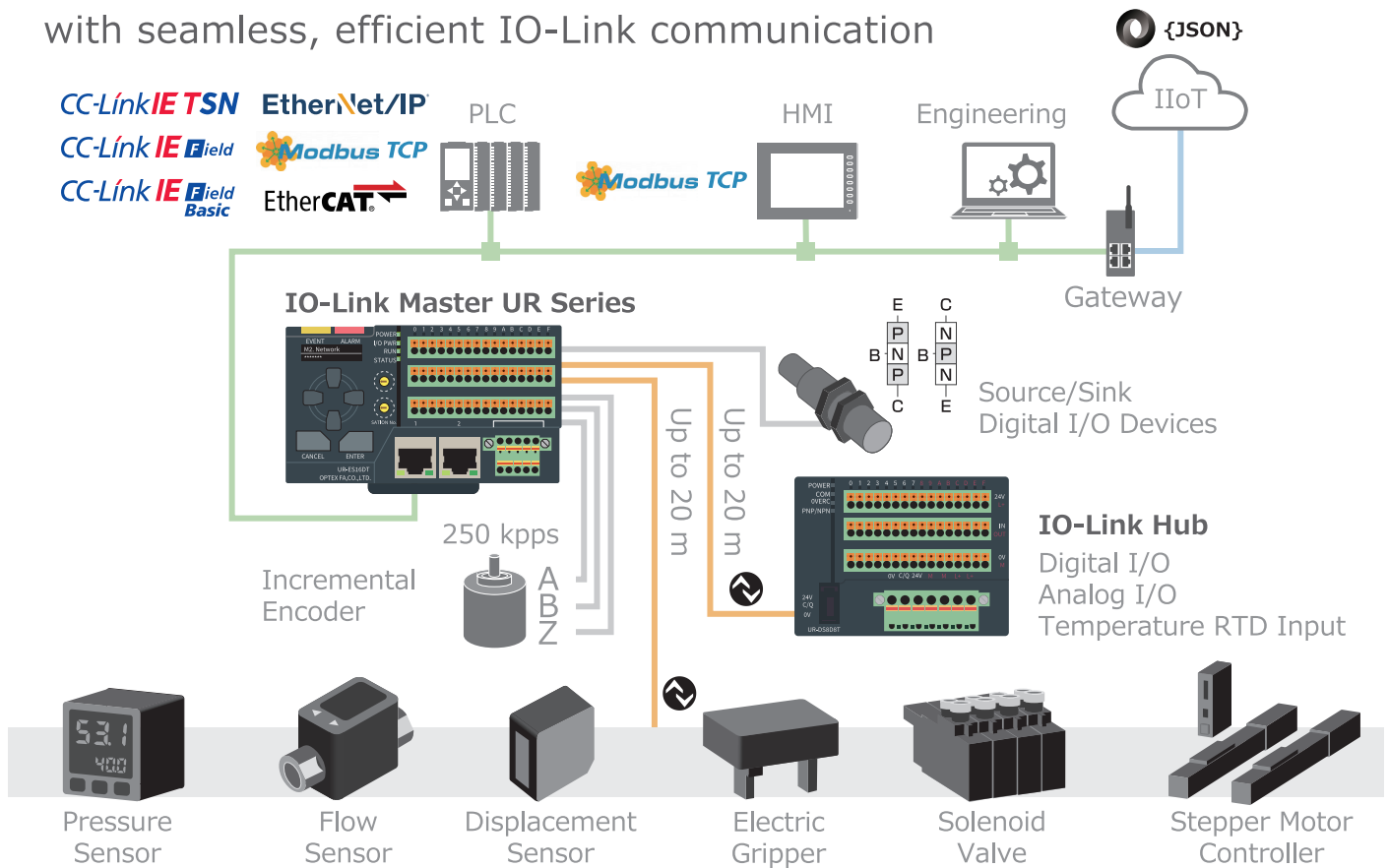
*The achievable cycle time is subject to the performance of the IO-Link device.






■ Each of the 16 ports can be individually configured as:

- IO-Link Master ports
- High-speed counter with 250 kpps in single, 2 or 3-phase
- Source or Sink digital input
- Source or Sink digital output

System overview

The smart solution replacing traditional Analog and Digital I/O with seamless, efficient IO-Link communication



Type	Description	Model
IO-Link Masters (Spring Clamp Terminal)		
	EtherNet/IP, EtherCAT, Modbus TCP, CC-Link IE Field Basic, JSON 16-port: IO-Link/DIO (Source, Sink)/High-speed counter (Single, 2 or 3-phase)	UR-ES16DT
	CC-Link IE TSN, CC-Link IE Field 16-port: IO-Link/DIO (Source, Sink)/High-speed counter (Single, 2 or 3-phase)	UR-MS16DT
IO-Link Masters (e-CON Connector)		
	EtherNet/IP, EtherCAT, Modbus TCP, CC-Link IE Field Basic, JSON 16-port: IO-Link/DIO (Source, Sink)/High-speed counter (Single, 2 or 3-phase)	UR-EE16DT
	CC-Link IE TSN, CC-Link IE Field 16-port: IO-Link/DIO (Source, Sink)/High-speed counter (Single, 2 or 3-phase)	UR-ME16DT
IO-Link Hubs		
	16x DI (Source, Sink)	UR-DS16D
	16x DO (Source, Sink) Digital source output: 0.5 ... 4 A per output, with a maximum total output of 12 A	UR-DS16T
	8x DI, 8x DO (Source, Sink) Digital source output: 0.5 ... 4 A per output, with a maximum total output of 12 A	UR-DS8D8T
	4x AI Voltage: 0 ... 10 V, -10 ... 10 V, 0 ... 5 V, 1 ... 5 V Current: 0 ... 20 mA, 4 ... 20 mA	UR-DS4AD
	4x AO Voltage: 0 ... 10 V, -10 ... 10 V, 0 ... 5 V Current: 0 ... 20 mA	UR-DS4DA
	8x RTD input (Pt100)	UR-DS8RTD